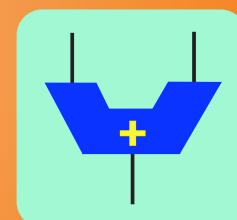# VDAT-2017

21st International Symposium on VLSI Design and Test

Department of Electronics and Communication Engineering

Indian Institute of Technology Roorkee, India

29th June - 2nd July 2017

VLSI SOCIETY OF INDIA

Call for Tutorials

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About VDAT

VDAT began as a small workshop in the year 1998. In 2005, it acquired the status of a Symposium. The purpose of the Symposium is to promote the advancement of all aspects of VLSI technology, primarily in India. The twenty-first International Symposium on VLSI Design and Test (VDAT-2017) to be held in Indian institute of Technology, Roorkee, India. The aim of this symposium is to bring together academics, researchers and industrial practitioners to exchange their ideas in the field of VLSI design and testing.

About IIT Roorkee

Indian Institute of Technology - Roorkee is among the foremost of institutes of national importance in higher technological education and in engineering, basic and applied research. The Institute's inception was in 1847 as Thomson College of Engineering. Since its establishment, the Institute has played a vital role in providing the technical manpower and know-how to the country and in pursuit of research. The Institute ranks amongst the best technological institutions in the world and has contributed to all sectors of technological development. It has also been considered a trend-setter in the area of education and research in the field of science, technology, and engineering.

The Institute has completed 150th year of its existence on October 1996. On September 21, 2001, an Ordinance issued by the Government of India declared it as the nation's seventh Indian Institute of Technology. The Ordinance is now converted into an Act by the Parliament to make IIT, Roorkee as an "Institution of National Importance".

About ECE Department

The Electronics and Communication Engineering (ECE) Department has a rich tradition in research and teaching. The research interests of the faculty members of the department encompass the wide area of applied and fundamental aspects of Electronics and Communication Engineering. Collaboration in between the research groups of the department and with other departments is also a practice. The broad areas of research and academic activities in the department are Microelectronics & VLSI, Communication Systems and RF & Microwave Engineering.

Important Dates / Deadlines

Regular Paper
Last Date for Paper Submission	:	27th February 2017
Notification of Acceptance	:	25th April 2017
Camera Ready Version	:	7th May 2017

Tutorial
Tutorial Proposal Submission	:	15th March 2017
Tutorial Announcement	:	15th April 2017

Work-In-Progress (WIP) Forum
WIP Submission	:	5th March 2017
Notification of Acceptance	:	15th April 2017
Camera Ready Version	:	25th April 2017

Registration Details

For paper submission, registration and other details, please visit
www.iitr.ac.in/vdat2017

Please submit a single .pdf file of your proposal to
vdat2017@iitr.ac.in

Call for Tutorial

All Researchers, professionals and academicians are invited to present full day (6 hrs) or half day (3 hrs) tutorials at VDAT-2017.

Tutorial Topics (not limited to)

- Circuit and Device Interaction
- Emerging Devices and Technologies
- Digital, Analog / Mixed Signal Design
- VLSI circuit Technology
- Memories
- Optoelectronics
- Modeling and Simulation of Devices, Circuit and Systems
- Sensor / MEMS
- CAD for VLSI
- Organic Electronics
- VLSI System Level Design VLSI Architecture

Submission Details

Title of the Tutorial
The title will appear in the symposium program if the proposal is accepted.

Abstract of the Tutorial
This abstract will be used to advertise the proposal, for instance, on the symposium's web site prior to the symposium. The abstract should be 250 words long at the most.

Proposed duration: Half-day tutorials will run for a total of about 3 hours; full day tutorials will run for about 6 hours.

Prerequisite Knowledge
What the attendees should already know.

Detailed Outline
The topics covered in the tutorial will be listed and briefly discussed in the outline, along with the amount of time planned for each topic. This should be limited to 2500 words.

Tutorial Goal.
This is a short message on the tutorial's goals and benefits to prospective participants.

Background information of the presenter(s) should be limited to 1-2 pages and contain-
Names, affiliations, homepages and contact details.
Short biographies including related recent publication.
Information about previous tutorials given by the same presenters
(title, location, number of attendees, etc.).