

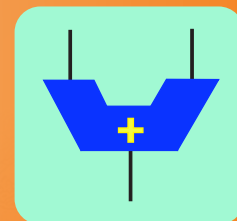
V DAT-2017

21st International Symposium on VLSI Design and Test

Department of Electronics and Communication Engineering

Indian Institute of Technology Roorkee, India

29th June - 2nd July 2017



VLSI SOCIETY OF INDIA



Call for Papers

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If you have any query please contact:
vdat2017@iitr.ac.in

About VDAT

VDAT began as a small workshop in the year 1998. In 2005, it acquired the status of a Symposium. The purpose of the Symposium is to promote the advancement of all aspects of VLSI technology, primarily in India. The twenty-first International Symposium on VLSI Design and Test (VDAT-2017) to be held in Indian Institute of Technology, Roorkee, India. The aim of this symposium is to bring together academics, researchers and industrial practitioners to exchange their ideas in the field of VLSI design and testing.

About IIT Roorkee

Indian Institute of Technology - Roorkee is among the foremost of institutes of national importance in higher technological education and in engineering, basic and applied research. The Institute's inception was in 1847 as Thomson College of Engineering. Since its establishment, the Institute has played a vital role in providing the technical manpower and know-how to the country and in pursuit of research. The Institute ranks amongst the best technological institutions in the world and has contributed to all sectors of technological development. It has also been considered a trend-setter in the area of education and research in the field of science, technology, and engineering.

The Institute has completed 150th year of its existence on October 1996. On September 21, 2001, an Ordinance issued by the Government of India declared it as the nation's seventh Indian Institute of Technology. The Ordinance is now converted into an Act by the Parliament to make IIT, Roorkee as an "Institution of National Importance".

About ECE Department

The Electronics and Communication Engineering (ECE) Department has a rich tradition in research and teaching. The research interests of the faculty members of the department encompass the wide area of applied and fundamental aspects of Electronics and Communication Engineering. Collaboration in between the research groups of the department and with other departments is also a practice. The broad areas of research and academic activities in the department are Microelectronics & VLSI, Communication Systems and RF & Microwave Engineering.

Important Dates / Deadline

Regular Paper

Last date for Paper Submission : **27th February 2017**
Notification of Acceptance : **25th April 2017**
Camera Ready Version : **7th May 2017**

Tutorial

Tutorial Proposal Submission : **15th March 2017**
Tutorial Announcement : **15th April 2017**

Work-In-Progress (WIP) Forum

WIP Submission : **5th March 2017**
Notification of Acceptance : **15th April 2017**
Camera Ready Version : **25th April 2017**

The paper accepted for oral presentation will be recommended to IEEE Conference Publication Program (CPP) to include it on the IEEEExplore website provided the following conditions are fulfilled: (a) it strictly complies with IEEE conference paper formatting guidelines, (b) the paper is presented in the conference by at least one of the registered authors and (c) the filled and signed copy-right transfer form is submitted along with the paper.

Registration Details

For Paper Submission, registration and other Details, please visit

www.iitr.ac.in/vdat2017

The papers are to be submitted through the easy chair link :

<https://easychair.org/conferences/?conf=vdat2017>

Call For Papers

Researchers, academicians and professionals are invited to submit papers in the following topics (but not limited to)

Device - Circuit Interactions

- Digital / Analog Circuits.
- Technology - circuits Co-Design.
- Impact of Novel Device Structure/ phenomena on Circuits.
- Device Modeling and Simulation
- Multi-gate/ FDSOI device-circuit interaction.

VLSI Design

- CMOS Analog, Mixed-Signal and digital circuits.
- Circuit timing and power models.
- Low Power and near/sub threshold circuit design
- Analog, Mixed-Signal and digital system design.
- Reconfigurable logic/computation.
- Design for manufacturability.

Emerging Technologies

- MeMs / NeMs
- Organic Electronic
- Spintronics
- 2-D material electronics.
- Device physics design and circuits using non- silicon materials

CAD for VLSI

- Logic and behavioural synthesis.
- Placement, routing and floorplanning.
- CAD tools.
- Design Automation.

Testing and Verification

- Design Verification, Test, Reliability and Fault Tolerance
- Formal Verification
- DFT
- Fault Modeling
- Post-silicon Validation
- Testing memories and regular logic arrays.
- Design for manufacturability and yield analysis.

There will be double blind review of the paper. Therefore do not include authors' name in the submitted paper. A Paper with authors' names will not be considered for review. The write-up should also not disclose the identity of the author/s. The paper must include an abstract of about 250 words and maximum of five keywords related to the topic of the paper. The acceptance of the paper is based on the following factors: The purpose of the work; The manner and degree to which it advances the art; Specific new results that have been obtained and their significance.

