

The following faculty members are interested to take Ph.D. students from Spring 2017-18 interview, to be held on 1st Nov. 2017. Their research areas are mentioned against their names.

Choice No.	Specialization	Faculty	Research Areas
[1]	Communication Systems	Dr. Debashis Ghosh Dr. Pyari Mohan Pradhan	Communication Systems & Signal Processing, Cognitive radio & Sensor networks, Image & video processing, Computer vision & pattern recognition Wireless Communication, Cognitive Radio Time-Frequency Representation, Time-Frequency Transforms Wireless Sensor Network, Distributed Data Estimation
[2]	Microelectronics and VLSI	Dr. Sudeb Dasgupta Dr. Arnab Datta	Ultra Low Power, Adiabatic Logic for Portable Applications Low Power Application, Subthreshold Logic Design Radiation Effects on ICs, Design and Development of 6T FinFET Based Rad Hard SRAM Cell Novel Semiconductor Devices, FinFETs, PDSOI, FDSOI Nanoelectronics, Semiconductor Device Modelling Micro (Nano) Electronic Devices: Electrical Characterization and Modeling of Semiconductor Devices, Non-Volatile Memories, Advanced MOSFET Reliability, MOS Device Fabrication MEMS: Electro-mechanical Switches, Device Failure Mechanism Photonics: Semiconductor Photonic Devices, Novel Photonic Devices

[3]	RF and Microwave Engineering	<p>Dr. M. V. Kartikeyan</p> <p>Dr. Rajib K Panigrahi</p> <p>Dr. Karun Rawat</p>	<p>High-power Millimeter/THz Wave Engineering, Sources and Allied Components Metamaterials and Fractals, Application in Planar Antennas, Filters and Microwave Circuits, Antenna Engineering/MICs, Printed Antennas, Filters and Allied Passive Components, Soft-computing, Soft-computing in Microwave Domain</p> <p>Information extraction from radar images, Radar signal processing, Target detection and estimation, Radar based remote sensing</p> <p>Power Amplifiers:, Broadband, load modulation, Doherty PA, pulse gate modulation, High Efficient PAs, Switch Mode PAs Transceiver Design: All-digital transmitters, Transceivers, Digital control of RF circuits, Transmit/Receive module Chip Design: CMOS and GaN MMIC based power amplifiers design, mixed signal design, GaN MMIC based RF Chip design Modelling: RF Power Amplifier Design with Embedding Device Model, Non-linear device modeling RF Circuits: Multi-port Networks, Modulators, RF Analog Processing, mixers, LNA Embedded Design: FPGA based transceivers for wireless applications Sensor Integration with IOT: IOT based pedestrian counting, Cloud centric IOT Wireless Communication using embedded platforms: Software defined radio for Wireless communication.</p>
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